<EJ/IS/IE/DE>: <22636>: <Emerging Trends in Electronics>: <Advance Processors>: < UO1e: Compare ARM7 and ARM9 TDMI processor>: <Assessments>: <Formative>

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| Set 1: Question No 1 | Set 1: Question No 2 | Set 1: Question No 3 |
| Pipeline depth of ARM 7 processor family is | In ARM processor when Interrupt occurs ARM processor goes into following mode | The performance improvement of processor is achieved by |
| Recall/ Remembering | Understanding | Application |
| 1. 3 stage | 1. FIQ mode | 1. Increase in frequency |
| 1. 5 stage | 1. Abort mode | 1. Increase in I/O ports |
| 1. 8 stage | 1. Supervisor mode | 1. Increase in memory capacity |
| 1. 2 stage | 1. Undefined mode | 1. Increase in number of stages in pipeline |
| Ans: < a > | Ans: < a > | Ans: < d > |

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| Set 2: Question No 1 | Set 2: Question No 2 | Set 2: Question No 3 |
| Pipeline depth of ARM 9 processor family is | ARM 9 operates on frequency \_\_\_\_\_\_\_\_ than ARM 7. | The Embedded lCE functionality in the ARM9TDMI core gives |
| Recall/ Remembering | Understanding | Application |
| 1. 3 stage | 1. Lower | 1. system power down |
| 1. 5 stage | 1. Higher | 1. System reset |
| 1. 8 stage | 1. Under | 1. System level debug |
| 1. 2 stage | 1. further down | 1. System format |
| Ans: < b > | Ans: < cb > | Ans: < c > |